Madasha Bararan Baharran Arkadasa		U.S. Paten	t and Tr	rademark Of	fice: U.S.	PTO/SB/21 (08-03) hugh 08/30/2003. OMB 0651-0031 DEPARTMENT OF COMMERCE	
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FORM (to be used for all correspondence after initial filing)		First Named Inventor Stephon R Cebenko Art Unik 2818					
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		Examiner Name	Dao H	Nguyen		· · · · · · · · · · · · · · · · · · ·	1
Total Number of Pages in This Submission	11	Attorney Docket Number	1-1-3-1)
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Response to Missing Parts/						FEB 0 4 2	
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Date 2/4/2004						·	1
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I hereby certify that this correspondence is b sufficient postage as first class mail in an en- the date shown below.	eing facsir	nile transmitted to the USPTO or o	leoosit	ed with the	United 60, Alexa	States Postal Service with andria, VA 22313-1450 on	
Typed or printed name MARK L BECKER	0-	$-\Omega$					
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FEE TRANSMITTAL		Complete if Known							
LEE IKANSMITTAI	– [Application Number 10/08:				2,050]		
for FY 2004		Filing Date 2/14/2			2/14/2	2002			
Effective 10/01/2003. Patent fees are subject to annual revision.	First Named Inventor Steph				tor Stephe	en R Cebenko			
				ame	Dao H	H Nguyen			
Applicant claims small entity status. See 37 CFR 1.27	Art Unit 2818				2818				
TOTAL AMOUNT OF PAYMENT (\$) 110	Attorney Docket No. 1-1-3-				lo. 1-1-3-1	1-1			
METHOD OF PAYMENT (check all that apply)	FEE CALCULATION (continued)								
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1001 770 2001 385 Utility filing fee		2,010	2255			reply within fifth month			
1002 340 2002 170 Design filing fee	1401	330	2401		Notice of Appe				
1003 530 2003 265 Plant filling fee	1402	330	2402			support of an appeal			
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2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE		1,330	2501		Utility issue fee	•			
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SUBTOTAL (2) (\$)	Other fee (specify) "Reduced by Basic Filing Fee Paid SUBTOTAL (3) (\$) 110								
a number previously paid, if greater, For Reissues, see above									
SUBMITTED BY (Complete (# applicable)) Name (Print/Date) MARK RECKED) Registration No. (21205 Talenham 502 252 3520									
Name (Print/Type) MARK L BECKER		ttomey/		313	25	Telephone 503-268-8629			
Signature Market Septem						Date 2/4/2004			

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PAGE 2/11* RCVD AT 2/4/2004 5:41:13 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/1* DNIS:8729306 * CSID:503 268 8077 * DURATION (mm-ss):03-16

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FEB 0 4 2004

OFFICIAL

First Named Inventor: Stephen R. Cebenko

Application No.: 10/082,050

Filed: 02/14/2002

For: INTEGRATED CIRCUIT BASE

TRANSISTOR STRUCTURE AND

ASSOCIATED PROGRAMMABLE CELL

LIBRARY

Examiner: Dao H Nguyen

Art Unit: 2818

Attorney Docket No.: 1-1-3-1-1

CERTIFICATE OF TRANSMISSION

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being filed with the United States Patent and Trademark Office by facsimile transmission to 703-

872-9306 on February 4, 2004.

Mark L. Becker Attorney for Applicants

RESPONSE

Applicants respond to the Office action mailed 10/23/2003, as follows.

one of the multiplous of the base transistor structures, arranged immediately adjacent on another, is configurable to provide gate isolation for active transistors in other ones of the base transistor structures." The claimed gate isolation is electrical isolation provided by tying the separate gates of an intermediate base transistor structure to VDD and VSS, as explained in the amended paragraph. In contrast, the isolation described in Shaw is the physical isolation of transistors' diffused regions (sources and drains). Col. 29, lines 37-45 and the Abstract.

Shaw further does not disclose the limitations of dependent claims 7-17 or of new claims 21-22.

For the above reasons, applicants submit that the present claims are in condition for allowance, and respectfully request such action.

Please call the undersigned if he can be of any assistance in this case.

By: Wela

Respectfully submitted,

Date:

Mark L. Becker

Associate General Counsel, IP

Reg. No. 31,325

Lattice Semiconductor Corporation 5555 NE Moore Ct. Hillsboro, OR 97124

Phone: 503-268-8629 Fax: 503-268-8459

mark.becker@latticesemi.com